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Designing with the NiosII Processor and SOPC Builder

Further, SoPC systems are the programmable embedded systems i.e. we can design the embedded systems using Verilog/VHDL codes and implement them on the FPGA board. Nios II is a 32-bit embedded-processor architecture designed for Altera-FPGA board. In Fig. 12.1, various steps are shown to design the SoPC system on the FPGA board using Nios-II ...

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